

# Consistency of circuit lower bounds with bounded theories

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Talk based on joint works with **Jan Bydžovský** (Vienna) and **Jan Krajíček** (Prague).

**Theoretical Computer Science Seminar – University of Birmingham**

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# Computational Complexity Theory

- ▶ Investigates limits and possibilities of algorithms and computations.

**P vs BPP:** Are *randomised* algorithms significantly faster than *deterministic* algorithms?

**P vs NP:** Is *finding* a solution harder than *verifying* a given solution?

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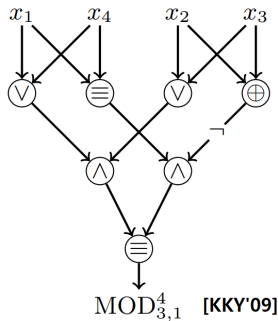
**P vs BPP:** Are *randomised* algorithms significantly faster than *deterministic* algorithms?

**P vs NP:** Is *finding* a solution harder than *verifying* a given solution?

- ▶ **Uniform computations:** single algorithm that works on all input lengths.

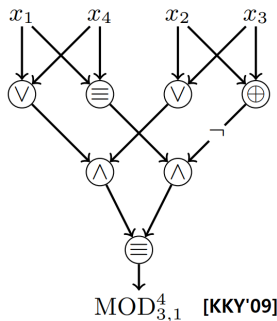
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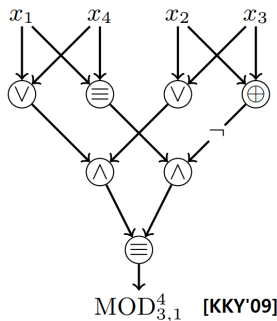


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- ▶ **Non-uniform computations:**

Sequence  $\{C_n\}_n$  of circuits, where  $C_n$  solves the problem on  $n$ -bit input instances.

- ▶ Algorithm running in time  $T(n) \implies$  circuits  $C_n$  with  $O(T(n) \cdot \log T(n))$  gates.

# Circuit Complexity Theory

- ▶ Interested in circuit size (number of gates) required to compute  $f: \{0, 1\}^n \rightarrow \{0, 1\}^m$ .

**[Shannon'49]** Most functions  $f: \{0, 1\}^n \rightarrow \{0, 1\}$  require circuits of size  $\Omega(2^n/n)$ .

- ▶ In connection to algorithms and complexity, we would like to understand the circuit size of “explicit” functions in P, NP, etc.

# Research on restricted classes of circuits

- ▶ Much progress over the last 40 years in understanding limited classes of circuits, such as **small-depth** circuits with AND/OR/NOT gates.
  - **Addition** of two  $n$ -bit numbers is *provably easier* than **Multiplication**.
  - **$\text{DIST}_k\text{-CONNECTIVITY}(n)$**  requires depth- $d$  circuits of size  $n^{k^{\Theta(1/d)}}$ .
  - The constant-depth circuit complexity of  **$k$ -CLIQUE** is precisely  $n^{\Theta(k)}$ .



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  - The constant-depth circuit complexity of  **$k$ -CLIQUE** is precisely  $n^{\Theta(k)}$ .
- ▶ However, many important algorithms produce circuits of **unbounded** depth.

# Status of circuit lower bounds

- ▶ In this talk we will focus on **unrestricted** Boolean circuits.
- ▶ Best result for a problem in NP is a lower bound of  $(3 + 1/86) \cdot n$  gates **[FGHK'16]**.

# Status of circuit lower bounds

- ▶ In this talk we will focus on **unrestricted** Boolean circuits.
- ▶ Best result for a problem in NP is a lower bound of  $(3 + 1/86) \cdot n$  gates **[FGHK'16]**.
- ▶ Proving a lower bound such as  $\text{NP} \not\subseteq \text{SIZE}[n^2]$  seems out of reach.
- ▶ Motivates the study of circuit lower bounds for classes believed to be larger than NP.

$\text{ZPP}^{\text{NP}} \not\subseteq \text{SIZE}[n^k]$  [Kobler-Watanabe'90s]

$\text{MA}/1 \not\subseteq \text{SIZE}[n^k]$  [Santhanam'00s]

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► **Frontier 1:** Lower bounds for **deterministic** class  $P^{NP}$ ?

While we have lower bounds for larger classes, **there is an important issue:**

► **Frontier 2:** All results of the form  $\omega(n)$  only hold on **infinitely many input lengths**.

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- ▶ **Mystery:** Existence of mathematical objects of certain sizes making computations easier only around corresponding input lengths.
- ▶ Issue **not** restricted to complexity lower bounds:

Sub-exponential time generation of canonical prime numbers [Oliveira-Santhamam'17].



# The logical approach

► We discussed two frontiers in complexity theory:

1. Understand relation between  $P^{NP}$  and say  $SIZE[n^2]$ .
2. Establish almost-everywhere circuit lower bounds.

► This work investigates these challenges from the **perspective of mathematical logic**.

## Investigating complexity through logic

- ▶ Theories in the standard framework of first-order logic.
- ▶ Investigation of complexity results that can be established under certain axioms.

**Example:** Does theory T prove that SAT can be solved in polynomial time?

- ▶ **Complexity Theory** that considers **efficiency** and **difficulty of proving correctness**.

# Bounded Arithmetics

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**Example:** **Theory**  $I\Delta_0$  [Parikh'71].

$I\Delta_0$  employs the language  $\mathcal{L}_{PA} = \{0, 1, +, \cdot, <\}$ .

14 axioms governing these symbols, such as:

1.  $\forall x \ x + 0 = x$
2.  $\forall x \forall y \ x + y = y + x$
3.  $\forall x \ x = 0 \vee 0 < x$
- ...

# Bounded formulas and bounded induction

**Induction Axioms.**  $I\Delta_0$  also contains the induction principle

$$\psi(0) \wedge \forall x (\psi(x) \rightarrow \psi(x+1)) \rightarrow \forall x \psi(x)$$

for each **bounded formula**  $\psi(x)$  (additional free variables are allowed in  $\psi$ ).

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► This shifts the perspective from computability to complexity theory.

## Theories $PV$ , $S_2^1$ , and $T_2^1$

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$$PV \approx T_2^0 \subseteq S_2^1 \subseteq T_2^1 \subseteq S_2^2 \subseteq T_2^2 \subseteq \dots \subseteq \bigcup_i T_2^i \approx I\Delta_0 + \Omega_1$$

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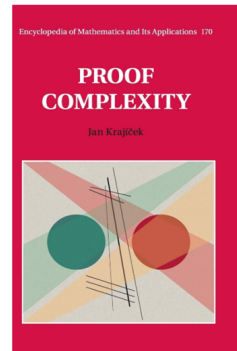
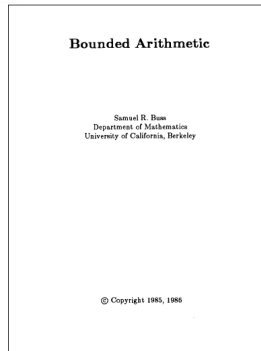
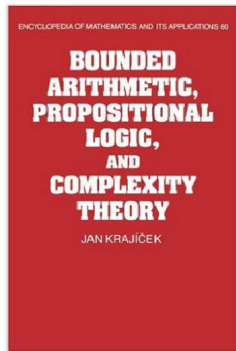
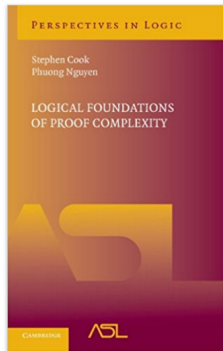
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► This places **Integer-Factoring in P**, which contradicts cryptographic assumptions.

# Resources I





## Resources II

### ► Some PhD Theses:

Kerry Ojakian (CMU, 2004). *Combinatorics in Bounded Arithmetic*.

Emil Jerabek (Prague, 2005). *Weak Pigeonhole Principle and Randomized Computation*.

Dai Tri Man Le (Toronto, 2014). *Bounded Arithmetic and Formalizing Probabilistic Proofs*.

Jan Pich (Prague, 2014). *Complexity Theory in Feasible Mathematics*.

### ► A recent work with pointers to several relevant references:

Moritz Muller and Jan Pich (2019). *Feasibly constructive proofs of succinct weak circuit lower bounds*.

# Formalizations in Bounded Arithmetic

- ▶ Many complexity results have been formalized in such theories.

Cook-Levin Theorem in PV [folklore].

PCP Theorem in PV [Pich'15].

Parity  $\notin \text{AC}^0$ ,  $k$ -Clique  $\notin \text{mSIZE}[n^{\sqrt{k}/1000}]$  in  $\text{APC}^1 \subseteq T_2^2$  [Muller-Pich'19].

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**Rest of the talk:** **Independence of complexity results** from bounded arithmetic.

# Unprovability and circuit complexity

► Using  $\mathcal{L}_{PV}$ , we can refer to circuit complexity:

$$\exists y (\text{Ckt}(y) \wedge \text{Vars}(y) = n \wedge \text{Size}(y) \leq 5n \wedge \forall x (|x| = n \rightarrow (\text{Eval}(y, x) = 1 \leftrightarrow \text{Parity}(x) = 1)))$$

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**Two directions:** unprovability of **LOWER** bounds and unprovability of **UPPER** bounds.

# Unprovability of circuit **LOWER** bounds

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**Motivation:** Why are complexity lower bounds so difficult to prove?

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**Example:** Is it the case that  $T_2^2 \not\vdash k\text{-Clique} \notin \text{SIZE}[n^{\sqrt{k}/100}]$  ?

- ▶ Extremely interesting, but not much is known in terms of **unconditional** unprovability results for strong theories such as PV.

## Unprovability of circuit **UPPER** bounds

- ▶ We currently cannot rule out that  $\text{SAT} \in \text{SIZE}[10n]$ . Can we at least show that easiness of SAT doesn't follow from certain axioms?

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  - $M$  satisfies many known results in algorithms and complexity theory.
3. **Consistency of lower bounds:** Adding to  $T$  axiom stating that SAT is hard will never lead to a contradiction. We can develop a theory where circuit lower bounds exist.

## Some works on unprovability of circuit upper bounds

- ▶ Cook-Krajicek, 2007: “*Consequences of the provability of  $\text{NP} \subseteq \text{P/poly}$* ”.

Initiated a systematic investigation. Conditional unprovability results.

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- ▶ Bydzovsky-Muller, 2018: “*Polynomial time ultrapowers and the consistency of circuit lower bounds.*”.

Model-theoretic proof of a slightly stronger statement.

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► Recall issue mentioned earlier in the talk:

We lack techniques to show hardness with respect to every large enough input length.

## This work

- ▶  $T_2^1$  and weaker theories cannot establish circuit upper bounds of the form  $n^k$  for classes contained in  $P^{NP}$ .
- ▶ Unprovability of infinitely often upper bounds, i.e., models where hardness holds almost everywhere.
- ▶ All results are **unconditional**.

# Our main result

**Theorem 1 (Informal):** For each  $k \geq 1$ ,

$$T_2^1 \not\vdash \text{P}^{\text{NP}} \subseteq \text{i.o.SIZE}[n^k]$$

$$S_2^1 \not\vdash \text{NP} \subseteq \text{i.o.SIZE}[n^k]$$

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**Extensions.**  $\text{True}_1 \stackrel{\text{def}}{=} \forall \Sigma_1^b(\mathcal{L}_{\text{PV}})$ -sentences true in  $\mathbb{N}$  can be included in first item.

**Example:**  $\forall x (\exists y (1 < y < x \wedge y \mid x) \leftrightarrow f_{\text{AKS}}(x) = 0)$

$T_2^1 \cup \text{True}_1$  proves that  $\text{Primes} \in \text{SIZE}[n^c]$  for some  $c \in \mathbb{N}$ , but not that  $\text{P}^{\text{NP}} \subseteq \text{i.o.SIZE}[n^k]$ .

## A more precise statement

- ▶  $\mathcal{L}_{PV}$ -formulas  $\varphi(x)$  interpreted over  $\mathbb{N}$  can define languages in P, NP, etc.
- ▶ The sentence  $\text{UB}_k^{\text{i.o.}}(\varphi)$  expresses that the corresponding  $n$ -bit boolean functions are computed infinitely often by circuits of size  $n^k$ :

$$\forall 1^{(\ell)} \exists 1^{(n)} (n \geq \ell) \exists C_n (|C_n| \leq n^k) \forall x (|x| = n), \varphi(x) \equiv (C_n(x) = 1)$$

### Theorem

*For any of the following pairs of an  $\mathcal{L}_{PV}$ -theory  $T$  and a uniform complexity class  $\mathcal{C}$ :*

- (a)  $T = T_2^1$  and  $\mathcal{C} = \text{P}^{\text{NP}}$ ,*
- (b)  $T = S_2^1$  and  $\mathcal{C} = \text{NP}$ ,*
- (c)  $T = \text{PV}$  and  $\mathcal{C} = \text{P}$ ,*

*there is an  $\mathcal{L}_{PV}$ -formula  $\varphi(x)$  defining a language  $L \in \mathcal{C}$  such that  $T$  does not prove the sentence  $\text{UB}_k^{\text{i.o.}}(\varphi)$ .*



# High-level ideas

- ▶ Two approaches (forget the “i.o.” condition for now):

$$\begin{array}{lcl} T_2^1 & \not\subseteq & \text{P}^{\text{NP}} \subseteq \text{i.o.SIZE}[n^k] \\ S_2^1 & \not\subseteq & \text{NP} \subseteq \text{i.o.SIZE}[n^k] \end{array}$$

Main ingredient is the use of **“logical” Karp-Lipton theorems**.

$$\text{PV} \not\subseteq \text{P} \subseteq \text{i.o.SIZE}[n^k]$$

Extract from (non-uniform) circuit upper bound proofs a **“uniform construction”**.

## Approach 1: “Logical” Karp-Lipton theorems

► A few unconditional circuit lower bounds in complexity theory use KL theorems. For instance,  $\text{ZPP}^{\text{NP}} \not\subseteq \text{SIZE}[n^k]$  can be derived from:

[Kobler-Watanabe'98] If  $\text{NP} \subseteq \text{SIZE}[\text{poly}]$  then  $\text{PH} \subseteq \text{ZPP}^{\text{NP}}$ .

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Better KL theorems in fact necessary in this case [Chen-McKay-Murray-Williams'19].

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**[Cook-Krajicek’07]** If  $\text{NP} \subseteq \text{SIZE}[\text{poly}]$  and **this is provable in a theory**  $T \in \{\text{PV}, S_2^1, T_2^1\}$ , then PH collapses to a complexity class  $\mathcal{C}_T \subseteq \text{P}^{\text{NP}}$ .

## Approach 2: A “bridge” between uniform and non-uniform circuits

If  $PV \vdash P \subseteq \text{SIZE}[n^k]$ , try to extract from PV-proof a “uniform” circuit family for each  $L \in P$ .

This would contradict known separation  $P \not\subseteq P\text{-unifom-SIZE}[n^k]$  [Santhanam-Williams'13].

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- ▶ Theorem 1 (c) strengthens Krajicek-Oliveira to rule out  $PV \vdash P \subseteq \text{i.o.SIZE}[n^k]$ .

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Complications appear because Santhanam-Williams doesn’t provide a.e. lower bounds.



## The unprovability result of Krajicek-Oliveira'17

The sentence  $\text{UB}_{k,c}(h)$  expresses that function symbol  $h$  admits circuits of size  $\leq cn^k$ .

**Theorem.** For every  $k \geq 1$  there is a PV function symbol  $h$  such that for no constant  $c \geq 1$  PV proves the sentence  $\text{UB}_{k,c}(h)$ .

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**Remark.**  $\text{UB}_{k,c}(h)$  is a  $\forall\exists\forall$ -sentence in  $\mathcal{L}_{\text{PV}}$ , and can be written as:

$$\text{UB}_{k,c}(h) \equiv \forall z \exists C \forall x \phi_h(z, C, x), \text{ where } \phi_h \text{ is quantifier-free.}$$

# The basic idea

- ▶ Logic/Provability as a bridge between non-uniform and uniform computations.

If  $PV \vdash UB_{k,c}(h)$  using a proof  $\pi$  (sequence of symbols), extract from  $\pi$  computational information about sequence  $C_n$  of circuits computing  $h$ .

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(We will later explain an important issue with this idea, and how it can be fixed.)

# The uniform lower bound

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► **Our Approach.** From a PV-proof of  $\text{UB}_{k,c}(h)$ , we try to extract a poly-time generating algorithm. We can't control its p-time bound, but this is okay with the theorem above!

# The KPT Witnessing Theorem

J. Krajíček, P. Pudlák, and G. Takeuti: “*Bounded arithmetic and the polynomial hierarchy*”, 1991.

**Theorem.** Assume  $T$  is a universal theory with vocabulary  $\mathcal{L}$ ,  $\phi$  is a quantifier-free  $\mathcal{L}$ -formula, and

$$T \vdash \forall z \exists C \forall x \phi(z, C, x) .$$

Then there exist a constant  $d \geq 1$  and a finite sequence  $t_1, \dots, t_d$  of  $\mathcal{L}$ -terms such that

$$T \vdash \phi(z, t_1(z), x_1) \vee \phi(z, t_2(z, x_1), x_2) \vee \dots \vee \phi(z, t_d(z, x_1, \dots, x_{d-1}), x_d).$$

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► The result can be established using proof theory or model theory.

## Applying the KPT Theorem to PV and $\text{UB}_{k,c}(f)$

- Fix  $k \geq 1$ , and assume that for every  $f \in \mathcal{L}_{\text{PV}}$  we have  $c \geq 1$  such that

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## The general case

- If  $d > 1$ , we obtain from  $PV \vdash UB_{k,c}(f)$  the more general scenario:

$$\mathbb{N} \models \phi(z, t_1(z), x_1) \vee \phi(z, t_2(z, x_1), x_2) \vee \dots \vee \phi(z, t_d(z, x_1, \dots, x_{d-1}), x_d).$$

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Either  $t_1(1^{(n)})$  outputs a correct circuit for  $f$ , or

There is a counter-example  $a_1 \in \{0, 1\}^n$ , and  $t_2(1^{(n)}, a_1)$  outputs a correct circuit, or

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- ▶ Due to the counter-examples, we can only show that  $f \in [P\text{-uniform} / O(n)]\text{-SIZE}(n^k)$ .
- ▶ **Contradiction? A difficulty is the lack of super-linear non-uniform lower bounds!**

## How to establish an unconditional unprovability result?

- ▶ Apply KPT to a specific  $\text{UB}_{k,c}(g)$ , obtaining a disjunction of  $\leq d$  formulas,  $d \in \mathbb{N}$ .  
(We will eliminate one by one in  $d$  stages, until we get a contradiction.)

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**The following ideas are crucial:**

- ▶ Using the constructivity of PV and Herbrand's Theorem, it can be shown that the counter-examples that previously caused difficulties can be provably witnessed in PV.



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Check the papers for more details!

**Parikh's Theorem.** Let  $A(\vec{x}, y)$  be a bounded formula.

If  $I\Delta_0 \vdash \forall \vec{x} \exists y A(\vec{x}, y)$  then  $I\Delta_0 \vdash \forall \vec{x} \exists y \leq t(\vec{x}) A(\vec{x}, y)$ .

## Bounded theories and a.e. vs i.o. circuit bounds

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► We use similar results to “tame” i.o. upper bounds in bounded arithmetic.

**Example:** If  $T_2^1 \vdash \text{SAT} \in \text{i.o.SIZE}[n^k]$  then  $T_2^1 \vdash \text{SAT} \in \text{SIZE}[n^{k'}]$ .

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- ▶ Not every language is paddable, and more delicate arguments are needed.

## Concluding Remarks: Logic and P vs NP

- ▶ A major question is to establish the unprovability of  $P = NP$ :

For a function symbol  $f \in \mathcal{L}_{PV}$ , consider the universal sentence

$$\varphi_{P=NP}(f) \stackrel{\text{def}}{=} \forall x \forall y \psi_{\text{SAT}}(x, y) \rightarrow \psi_{\text{SAT}}(x, f(x))$$

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**Conjecture.** For no function symbol  $f$  in  $\mathcal{L}_{PV}$  theory PV proves the sentence  $\varphi_{P=NP}(f)$ .

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**Conjecture.** For no function symbol  $f$  in  $\mathcal{L}_{PV}$  theory PV proves the sentence  $\varphi_{P=NP}(f)$ .

- ▶ Reduces to the study of unprovability of circuit **lower** bounds (Theorem 2 in our work).
- ▶ Motivates **both** research directions (**unprovability of upper and lower bounds**).



**Thank you**

# Krajíček's Fest

Celebrating Jan Krajíček's 60th Anniversary and his Contributions to Logic and Complexity

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